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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10069417	FILING DATE 02/25/2002	CLASS 430	SUBCLASS 300	GAU 1756	EXAMINER DWE
<b>**APPLICANTS:</b> Guggemos Michael; Kohnic Franz;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A 371 OF PCT/DE00/02423 07/18/2000					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> GERMANY 199449082 09/10/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 009-02	
TITLE : Method for producing a conductor pattern on a dielectric substrate					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	Application Examiner	
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